

PACKAGING STRUCTURE INTEGRATING PASSIVE DEVICES

CROSS-REFERENCE TO RELATED APPLICATION

This application claims the priority benefit of Taiwan application serial no.

5 90115213, filed June 22, 2001.

BACKGROUND OF THE INVENTION

Field of the Invention

[0001] The invention relates to a packaging structure. More particularly, the 10 present invention relates to a leadframe packaging structure that integrates passive devices.

Description of the Related Art

[0002] To eliminate negative electrical effects such as electro-magnetic interference or undesirable coupling between leads in a leadframe packaging structure, 15 passive devices are conventionally arranged on the leadframe of the leadframe packaging structure.

[0003] Referring to FIG. 1, a top view schematically illustrates a conventional leadframe structure used in packaging structures that integrate passive devices. At a central chip-bonding region 102 of the leadframe 100, a die pad 104 is conventionally arranged to carry a chip 106. Tie bars 108 are arranged around the die pad 104 as support elements to prevent warping of the die pad 104 during an encapsulating process. The 20 leadframe 100 further comprises leads 110 around the die pad 104 and dam bars 116. The dam bars 116 define an encapsulating region 117 by separating the leads 110 into inner leads 112 and outer leads 114.

[0004] FIG. 2 is an enlarged view of the region 118 shown in FIG. 1. As shown in FIG. 2, two adjacent inner leads 112a are conventionally extended to form a contact pad 120. Supported by two leads 112a, the contact pad 120 is more rigid. Furthermore, a peripheral portion of the die pad 104 extends outwardly to provide another contact pad 122. A passive device 124 thereby can be conventionally mounted between and electrically connects the contact pads 120, 122. The leadframe thus arranged can integrate a passive device within a packaging structure to improve the electrical proprieties of the packaging structure.

[0005] However, because the space between the die pad and inner leads of the conventional leadframe is limited, the possibility of passive device arrangement on the conventional leadframe is also limited. Such a space limitation allows for a decoupling of only two adjacent leads and more distant leads cannot be decoupled.

SUMMARY OF THE INVENTION

[0006] An aspect of the present invention is to provide a leadframe packaging structure that integrates passive devices and overcomes the above problems by re-arranging the leadframe structure.

[0007] To attain at least the foregoing objectives, a packaging structure integrating passive devices comprises, according to an embodiment of the present invention, a leadframe, a chip, at least a passive device, and an encapsulant material. The leadframe comprises a plurality of first leads that define a chip-bonding region within the leadframe. A die pad is arranged in the chip-bonding region. The chip is bonded on the die pad. A plurality of second leads extends and terminates in a plurality of contact pads in the chip-bonding region. An adhesive tape may be bonded onto bottom surfaces of the

contact pads. At least a passive device is mounted between and electrically connects the contact pads. A plurality of bonding wires electrically connects the chip, the passive device, and the first and second leads to one another. The encapsulant material encapsulates the chip, the bonding wires and the passive device, and leaves an outer portion of the leads exposed.

[0008] It is to be understood that both the foregoing general description and the following detailed description are exemplary, and are intended to provide further explanation of the invention as claimed.

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BRIEF DESCRIPTION OF THE DRAWINGS

[0009] The accompanying drawings is included to provide a further understanding of the invention, and are incorporated in and constitute a part of this specification. The drawings illustrate embodiments of the invention and, together with the description, serve to explain the principles of the invention. In the drawings,

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[0010] FIG. 1 is a top view schematically illustrating a conventional leadframe structure integrating a passive device;

[0011] FIG. 2 is an enlarged view of a portion of FIG. 2 showing the leadframe at the passive device mounting location;

20 [0012] FIG. 3 is a top view schematically illustrating a leadframe structure arranged to integrate passive devices according to an embodiment of the present invention;

[0013] FIG. 4 and FIG. 5 are enlarged views of a portion of FIG. 3 showing the leadframe at the passive device mounting location according to an embodiment of the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0014] The following detailed description of the embodiments and examples of the present invention with reference to the accompanying drawings are only illustrative of specific structures and ways of making of the present invention, and does not limit the scope of the present invention.

[0015] FIG. 3 is a top view schematically illustrating a leadframe structure for a semiconductor packaging structure integrating passive devices according to an embodiment of the present invention. A leadframe 200 includes a plurality of leads 210 that defines a chip-bonding region 202 substantially at a center part of the leadframe 200. A die pad 204 carrying a chip 206 is arranged within the chip-bonding region 202. The chip 206 may be bonded on the die pad 204 by, for example, adhesive tape having good heat dissipation ability. The location of the die pad 204 is biased to one side of the chip-bonding region 202 as shown in FIG. 3, where the die pad 204 is biased to the left side of the chip-bonding region 202.

15 [0016] As conventionally arranged, tie bars 208 connecting the die pad 204 to the leadframe 200 support the die pad 204. The tie bars 208 prevent the die pad 204 from bending when an encapsulating process is subsequently performed. A portion of the tie bars 208 further may be downwardly bent to down-set the die pad 204.

[0017] Reference numeral 216 refers to a dam bar that defines an encapsulating region 217 delimited by dash-lines, by separating the leads 210 into inner leads 212 and outer leads 214.

[0018] FIG. 4 shows an enlarged view of the region 218 shown in FIG. 3. As shown in FIG. 4, inner leads 212a, 212b, 212c extend toward the chip-bonding region 202. More particularly, the inner lead 212a terminates in contact pads 220a, 220b within the

chip-bonding region 202, and inner lead 212b and 212c respectively terminate in contact pads 220c and 220d within the chip-bonding region 202. The different inner leads 212a, 212b, 212c may be substantially distant from one another. Because the position of the die pad 204 is biased, the contact pads 220a, 220b, 220c, 220d can be arranged to converge in

5 the chip-bonding region 202 such that the passive devices can be mounted thereon. An adhesive tape 222 may be further adhered on bottom surfaces of the contact pads 220a, 220b, 220c, 220d to provide better rigidity and substantially a same surface level of the contact pads 220a, 220b, 220c, 220d. The adhesive tape 222 preferably is an insulating material having good heat resistance such as polyimide to endure subsequent wire bonding and encapsulating processes.

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[0019] As shown in FIG. 3 and FIG. 4, a passive device 224 via its contact pads 226 connects the contact pads 220b, 220d. The connection of the passive device 224 can be performed by known methods such as surface mount technology (SMT). The integration of passive device 224, such as a resistor, capacitor or inductor, improves electrical proprieties of the packaging structure. It is apparent that an appropriate arrangement of the die pad 204 may enable the integration of more than one passive device within the packaging structure such as shown in FIG. 4 where two passive devices are integrated. An option can be, for example, reducing the size of the die pad 204 and biasing its position within the chip-bonding region 202.

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[0020] Referring to FIG. 4 and FIG. 5, two passive devices 224, for example, electrically connect the contact pads 220a, 220c and the contact pads 220b, 220d, respectively. A plurality of bonding wires 228 electrically connects the bonding pads 230 of the chip 206 arranged on the die pad 204 to the passive devices 224 and to other inner leads 212. The bonding wire 228 may connect the passive devices 224 by bonding onto

the contact pads terminating the leads. A wire-bonding pad 232 also may be formed on the inner lead 212a within the chip bonding region 202 to facilitate the wire bonding between the passive device 224 and bonding pad 230a of the chip 206. After wire bonding, the chip 206, passive devices 224 and bonding wires 228 are encapsulated 5 within an encapsulant material (not shown). Dam bars 216 (shown in FIG. 3) are cut and remaining outer leads 214 (shown in FIG. 3) are bent to achieve the packaging structure (not shown).

[0021] In conclusion, the present invention provides a leadframe structure capable of carrying chip and passive devices within packaging structures. The leadframe 10 of the present invention includes inner leads that extend and terminate in contact pads proximate to a die pad biased within a chip-bonding region. As a result, the passive devices can decouple various leads of the leadframe that may be substantially distant from one another. The present invention is suitable for use with leadframe packaging structures such as Quad Flat Package (QFP), Plastic Chip Carrier Package (PLCC), Dual 15 In-Line Package (DIP), Small Out-Line Package (SOP), etc.

[0022] It will be apparent to those skilled in the art that various modifications and variations can be made to the structure and operations of the present invention without departing from the scope or spirit of the invention.